

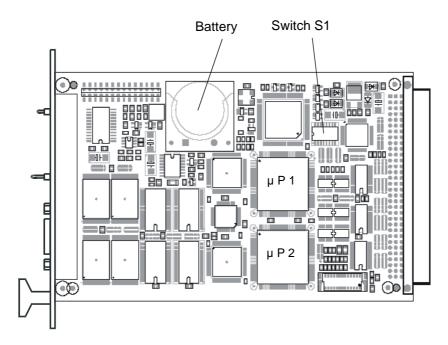
# F 8650X





# F 8650X: Central module

Use in the PES H51q-MS, -HS, -HRS, **Safety-related**, applicable up to SIL 3 according to IEC 61508



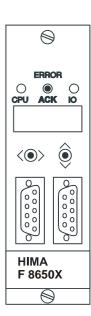


Figure 1: View

Construction

Central module with two clock-synchronized microprocessors

Microprocessors INTEL 386EX, 32 bits Clock frequency 25 MHz

Memory per microprocessor

Operating System Flash-EPROM 1 MB User program Flash-EPROM 1 MB \*

Data SRAM 1 MB \*

\* Degree of utilization depending on operating system version

Interfaces Two serial interfaces RS 485 with electric isolation
Diagnostic display Four digit matrix display with selectable information

Shutdown on fault Safety-related watchdog with output 24 V, loadable up to 500 mA, short-circuit proof

Two European standard PCBs,

one PCB for the diagnostic display

Space requirement 8 SU
Operating data 5 V / 2 A

## Setting of the bus station no. via switches S1-1/2/3/4/5/6/7:

Position switch no. 6 7							
	Off■ ■ Switch no.	Switch no		Switch no.		Switch no.	
Station 0	no. 1 2 3 4 5 On 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Station no. 1 2 3 4 missible 8 On		no. 1 2 3 4 5 On 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Station 24	no. 1 2 3 4 5 On 0 0 0 0 0	
1	On Off Off	9 On	17	On Off Off Off	25	On Off	
2	On Off	10 On Off	18	On Off Off	26	On Off Off Off	
3	On		19	On Off Off Off	27	On III III III	
4	On Off	12 On	20	On Off	28	On Off	
5	On Off	40 On ■ □ ■ ■ [	21	On Off	29	On Off Off Off Off Off Off Off Off Off O	
6	On Off	14 On	22	On Off	30	On Off	
7	On Off	15 On	23	On Off	31	On Off	
Position switch no. 6 7							
	Switch no.	Switch no		Switch no.	<b>.</b>	Switch no.	
Station 32	no. 1 2 3 4 5 On	Station no. 1 2 3 4 40 On	Station 48	no. 1 2 3 4 5 On	Station 56	no. 1 2 3 4 5 On 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
33	On Off	41 Off G		On Off	57	On Off	
34	On Off	42 On		On Off	58	On Off	
35	On Off	40 On ■ □ □ ■ [	51	On Off	59	On Off	
36	On Off	44 On □ □ ■ □ [	52	On Off	60	On Off	
37	On Off	45 Off	53	On Off	61	On Off	
38	On Off	46 On	54	On Off	62	On Off	
39	On S S S S S S S S S S S S S S S S S S S	47 On		On Off Off Off Off Off Off Off Off Off O	63	On Off	
Posi	tion switch no. 6 7						
	On ☐ ☐ ☐ Off ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐	Switch no		Switch no.		Switch no.	
	no. 1 2 3 4 5	Station no. 1 2 3 4	7	no. 1 2 3 4 5 On ∏ ∏ ∏ ■		no. 1 2 3 4 5	
64	Off	72 Off	80	Off	88	On Off Off Off Off Off Off Off Off Off O	
65	Off		81	Off	89	Off	
66	Off	74 On	7	On Off Off Off Off Off Off Off Off Off O	90	Off 📕 🗌 🖷 🗎 🗆	
67		75 On	83	Off	91	Off	
68 69	On Off Off Off Off Off Off Off Off Off O	76 On	84	Off II II II II II	92 93	On Off Off Off Off Off Off Off Off Off O	
70	On Off Off Off Off Off Off Off Off Off O	011 - 0 - 0	85 86	On	93	On Off Off Off Off Off Off Off Off Off O	
70				On	95	On Off Off Off	
	On Off Off Off Off Off Off Off Off Off O	Off L L L L	07	On Market Market	93	On Off U	
Posi	tion switch no. 6 7				_		
Station	Switch no. no. 1 2 3 4 5	Positions w	hite switch:				
96	On Off	On ☐ Bit is s	set On Off	Bit is not set			
97	On Off	White s	witch in	White switch in			
98	On Off	position	OFF	position ON			
99	On						

## Setting of the transmission rate with switch S1-8:

1 2 3 4 5 6 7 8		1 2 3 4 5 6 7 8	
On Off	S1-8 ON = 9600 bps		S1-8 OFF = 57600 bps

Pin	RS 485	Signal	Meaning
1	-	-	not used
2	-	RP	5 V, decoupled by diodes
3	A/A'	RxD/TxD-A	Receive/Transmit Data A
4	-	CNTR-A	Control signal A
5	C/C'	DGND	Data Ground
6	-	VP	5 V, positive pole of power supply
7	-	-	not used
8	B/B'	RxD/TxD-B	Receive/Transmit Data B
9	-	CNTR-B	Control signal B

Table 1: Pin assignment of the interface RS 485, 9-pole

For the serial interface only the bus station no. 1-31 can be set.

Within an Ethernet network the bus station no. can be set from 1 to 99. Therefore the switches S1-6/7 must be set in addition to the switches S1-1/2/3/4/5.

The number of the communication partners within a network is still limited to 64.

This enhanced setting of the bus station no. is only possible from operating system BS41q/51q V7.0-8 (05.31) of the central module.

### Applications with the communication module F 8627X:

- connection of the central module to a PADT (ELOP II TCP)
- connection to other communication partners within an Ethernet network (safeethernet, Modbus TCP)

The communication runs from the central module via the backplane bus to the communication module F 8627X and from the Ethernet ports of the F 8627X into the Ethernet network and vice versa.

#### Special features of the central module:

- Self-education: from operating system BS41g/51g V7.0-8 (05.31)
- ELOP II TCP: from operating system BS41q/51q V7.0-8 (05.31)

Further informations about the bus station no., ELOP II TCP, loading of operating systems and application programs (self-education) et al. corresponding to the central module you will find in the data sheet of the F8627X as well as the operating system manual of H41q/H51q and the safety manual of H41q/H51q.



Before removing a central module its fixing screws must be completely loosened and freely movable. Remove the module from the bus board by pushing the ejection lever (front label) top down and quickly removing in an upward motion to ensure that faulty signals are not triggered within the system!

To attach the module, place it on the terminal block and press it inwards as far as it will go. This action should be performed quickly to ensure that faulty signals are not triggered within the system!

### Function of the ejection lever with front label

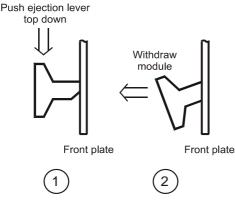


Figure 2: Function of the ejection lever

### Diagnostic display of the central module

- Four digit alphanumerical display,
- two LEDs for the general display of errors (CPU for the central modules, IO for the testable input/output modules),
- two toggle switches to request detailed error information,
- push-button ACK resets the error indication;
   in failure stop ACK behaves like restarting the system.

For further information on the diagnostic display and lists of error codes, refer to the documentation "Functions of the operational system BS 41q/51q" (also on ELOP II CD).

#### Notes for start-up and maintenance

- Lifetime of the buffer battery (without voltage feeding):
   1000 days at T<sub>A</sub> = 25 °C
   200 days at T<sub>A</sub> = 60 °C
- It is recommended to change the buffer battery (CPU in operation) at the latest after 6 years, or with display BATI within three months
   (Lithium battery, e.g. type CR 2477N, HIMA part no. 44 0000018)
- Check the bus station no. and transmission rate at switch S1 for correct settings
- Important: When upgrading an F 8650 to an F 8650X module the fan concept has also to be changed!